EPD10 VDSL Transformer

- EP10 SMD Package
- Meets the specification for supplemental insulation per: UL60950-1, CSA60950-1, EN60950-1, IEC60950-1 with a working voltage of 250V
- Operating Temperature range: -40ºC to +85ºC
- Maximum reflow temperature: 250ºC/-5ºC for 20-40 seconds (Refer to IPC/JEDEC J-STD-020C Standard)

### Electrical Specifications @25ºC

<table>
<thead>
<tr>
<th>Turn Ratio</th>
<th>Inductance</th>
<th>Leakage Inductance</th>
<th>Interwinding Capacitance</th>
</tr>
</thead>
<tbody>
<tr>
<td>(1-4)(8-5), (2+3, 6+7 short)</td>
<td>270uH±10%</td>
<td>1.0uH max.</td>
<td>105pF max.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Thermo-Coupling DCR</th>
<th>DCR</th>
<th>HIF</th>
<th>Longitudinal Balance</th>
</tr>
</thead>
<tbody>
<tr>
<td>@ 10KHz , 0.1 V (1-4) (2+3 short)</td>
<td>2.0Ω max.</td>
<td>-88dB max.</td>
<td>1875Vac /0.5mA/1sec.</td>
</tr>
<tr>
<td>@ 10KHz , 0.1 V (8-5) (6+7 short)</td>
<td>1.65Ω max.</td>
<td>-85dB max.</td>
<td>41dB min.</td>
</tr>
</tbody>
</table>

### MECHANICAL

- **Dimension**:
  - Suggested PCB Layout
  - DIMENSIONS: 12.5±0.2 x 15.0±0.2

### Marking

- ALLIED AEP10-3741
- YYWW = DATE CODE
- YY = YEAR; WW = WEEK

### SCHEMATICS

- Schematics showing the transformer connections and components.

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**All specifications subject to change without notice.**