VDSL LINE TRANSFORMER SURFACE MOUNT MAGNETICS

- EN60950 Basic Insulation compliant 250Vrms
- SMD compact foot print design
- Industrial operating temp: -40ºC to +85ºC

### Electrical Specifications @25ºC

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Turns Ratio @10kHz,0.1V (short 2-3, 5-6-7-8)</th>
<th>Inductance @10kHz,0.1V</th>
<th>Leakage Inductance @100kHz,0.1V</th>
<th>Cww @100kHz,0.1V</th>
<th>DCR @10kHz,0.1V</th>
<th>THD @100kHz,1.7V</th>
<th>Hi-Pot (1 Sec / 1mA)</th>
</tr>
</thead>
<tbody>
<tr>
<td>AEP035SI</td>
<td>1-4:8-5 (short 2-3, 5-6-7-8)</td>
<td>1-4</td>
<td>1-4</td>
<td>1-4</td>
<td>1-4</td>
<td>1-4</td>
<td>1-4 (short 2-3)</td>
</tr>
<tr>
<td></td>
<td>4:1±2%</td>
<td>109uH±10%</td>
<td>2.0uH max</td>
<td>70pF max</td>
<td>325mΩ ±15%</td>
<td>35mΩ ±25%</td>
<td>-78dB max</td>
</tr>
</tbody>
</table>

All specifications subject to change without notice.

### MECHANICAL

**SUGGESTED PAD LAYOUT**

Units:mm

Unless otherwise specified all tolerances are ±0.25 in mm

### SCHEMATICS

![Line Transformer Schematic](image)