EP5 ADSL Transformer AEP-108SI

- EP5 Design
- EN60950 Basic Insulation Compliant 250 Vrms
- SMD design providing compact footprint
- Industrial Operating Temp: -40°C to +85°C

### Electrical Specifications @25°C

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Turns Ratio @10KHz, 1V</th>
<th>Inductance Ls@500kHz, .1V</th>
<th>Leakage Inductance Ls@500kHz,1V</th>
<th>DCR</th>
<th>Hi-Pot VDC (1mA/1s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>AEP-108SI</td>
<td>(2-5):(1-6)</td>
<td>(3-4):(1-6)</td>
<td>1-6</td>
<td>1-6</td>
<td>2.5</td>
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<tr>
<td></td>
<td>1.70:1±2%</td>
<td>1.70:1±2%</td>
<td>2.4uH±10%</td>
<td>1.0uH max</td>
<td>50mΩ</td>
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<td>426mΩ</td>
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<td>468mΩ</td>
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<td>500</td>
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</tbody>
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All specifications subject to change without notice.

### MECHANICAL

- SUGGESTED PAD LAYOUT
- 6 SURFACES

Units:mm

Unless otherwise specified all tolerances are:±0.25 in mm

### SCHEMATICS

- PRI 5VDC@500KHz
- SEC 15VDC, 67mA DC
- SEC 15VDC, 67mA DC
- 6 SURFACES